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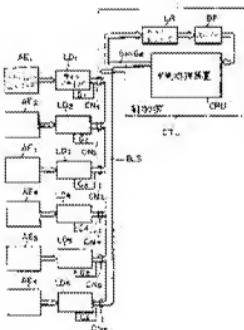
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(54) INTERFACE CIRCUIT FOR ABSOLUTE ENCODER

(57)Abstract:

PURPOSE: To make a circuit scale in the control part side small and a connecting condition for wiring simple by transferring absolute position signals for plural absolute encoders with a time division through a common bus-line.

CONSTITUTION: When the absolute position signal for the absolute encoder AE1 is read, a gate signal G1 is returned back to 'L' from 'H', when the output of a line-driver LD1 is made to have a high impedance. The absolute position signals for the absolute encoders AE2-AE6 of the 2nd-6th shafts are read in similarly by a central processing device CPU. The absolute position signals for size shafts are transferred with the time division through one bus-line BUS accordingly; therefore, a single line receiver LR and a buffer BF are enough to be provided in the control part CTL side, and a constitution of the control part CTL is thereby made to be in a small scale. Also, to the control part CTL, the bus-line BUS coming from one line-driver LD1 is only connected; so the wiring condition is not to be complicated and extremely simplified.



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